TD10F12-HAF

Surface Mount Fast Recovery Bridge Rectifier Reverse Voltage - 1000 V Forward Current - 1.2 A

Features

- · Glass Passivated Chip Junction
- · High Surge Current Capability
- Halogen and Antimony Free(HAF), RoHS compliant

4 Output Cathode (-) 4 ABF Package

DESCRIPTION
Input Pin (~)
Input Pin (~)

Output Anode (+)

PINNING

3

Mechanical Data

· Package: ABF

• Terminals: Solderable per MIL-STD-750, Method 2026

Maximum Ratings and Electrical characteristics

Single-phase, half-wave, 60 Hz, resistive or inductive load rating at 25°C, unless otherwise specified, for capacitive load, derate current by 20 %.

Davasatas	Symbols	TD10F12	Units
Parameter	Marking	F12FL10	-
Maximum Repetitive Peak Reverse Voltage	V_{RRM}	1000	V
Maximum RMS voltage	V _{RMS}	700	V
Maximum DC Blocking Voltage	V_{DC}	1000	V
Average Forward Current T _a = 50°C	I _{F(AV)}	1.2	А
Peak Forward Surge Current 8.3 ms Single Half Sine Wave Superimposed on Rated Load	I _{FSM}	50	Α
Maximum Instantaneous Forward Voltage at 1.2 A	V _F	1.1	V
Maximum DC Reverse Current at $T_a = 25 ^{\circ}\text{C}$ Rated DC Blocking Voltage $T_a = 125 ^{\circ}\text{C}$	I _R	5 50	μΑ
Typical Junction Capacitance 1)	C _j	30	pF
Maximum Reverse Recovery Time 2)	t _{rr}	500	ns
Operating and Storage Temperature Range	T _j , T _{stg}	- 55 to + 150	°C

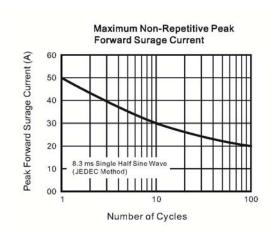
 $^{^{\}rm 1)}{\rm Measured}$ at 1 MHz and applied reverse voltage of 4 V D.C.

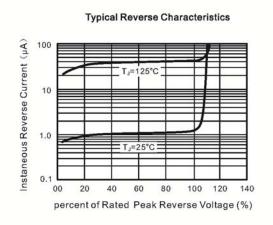


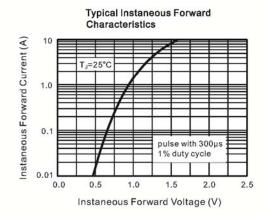


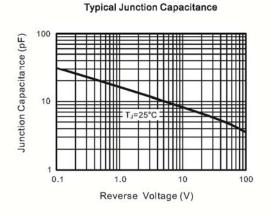


 $^{^{2)}\,\}text{Measured}$ with I_F = 0.5 A, I_R = 1 A, I_{rr} = 0.25 A.









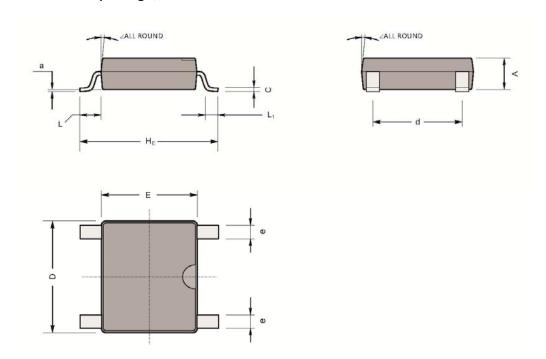






PACKAGE OUTLINE ABF

Plastic surface mounted package; 4 leads



UNIT	Α	С	D	Е	H _E	d	е	L	L1	а	
mm	1.2	0.22	5.2	4.5	6.4	4.2	0.7	0.95	0.6	0.1	7 °
	1	0.15	4.9	4.2	6	3.6	0.5				

Recommended Soldering Footprint

